SCHOTTKY BARRIER RECTIFIER

REVERSE VOLTAGE: 60 V
CURRENT: 3.0 A

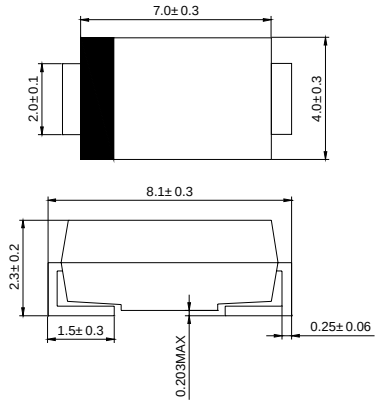
FEATURES

- ◇ Plastic package has Underwriters Laboratory Flammability Classification 94V-0
- ◇ For surface mounted applications
- ◇ Low profile package
- ◇ Built-in strain relief
- ◇ Metal silicon junction, majority carrier conduction
- ◇ High surge capability
- ◇ Low power loss,high efficiency
- ◇ For use in low voltage high frequency inverters,free wheeling and polarity protection applications
- ◇ Guardring for overvoltage protection
- ◇ High temperature soldering guaranteed:250°C/10 seconds at terminals

MECHANICAL DATA

- ◇ Case: NSMC,molded plastic over passivated chip
- ◇ Terminals:Solder plated, solderable per MIL-STD-750, Method 2026
- ◇ Polarity: Color band denotes cathode end
- ◇ Weight:

NSMC



Dimensions in millimeters

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified

		B360L	UNITS
Maximum recurrent peak reverse voltage	V_{RRM}	60	V
Maximum RMS voltage	V_{RWS}	42	V
Maximum DC blocking voltage	V_{DC}	60	V
Maximum average forw ord rectified current at T_L (SEE FIG.1)	$I_{(AV)}$	3.0	A
Peak forw ard surge current 8.3ms single half-sine-w ave superimposed on rated load(JEDEC Method)	I_{FSM}	80	A
Maximum instantaneous forw ard voltage at 3.0A(NOTE.1)	V_F	0.58	V
Maximum DC reverse current @TA=25℃	I_R	2.5	mA
at rated DC blocking voltage @TA=100℃		10	
Typical thermal resitance (NOTE. 2)	$R_{\theta JL}$	24	°C/W
Operating junction temperature range	T_J	-55---+150	°C
Storage temperature range	T_{STG}	-55---+150	°C

NOTE: 1.Pulse test:300μS pulse width,1%duty cycle
2. Thermal resitance junction to lead.

FIG.1 – FORWARD DERATING CURVE

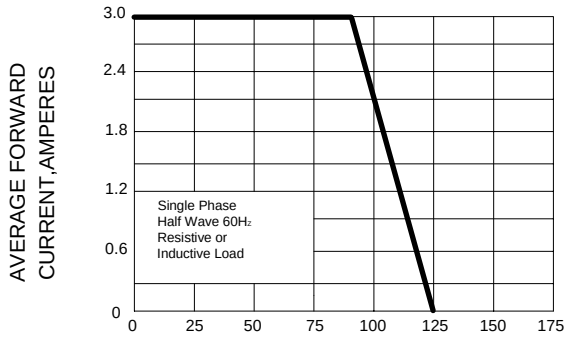


FIG.2– PEAK FORWARD SURGE CURRENT

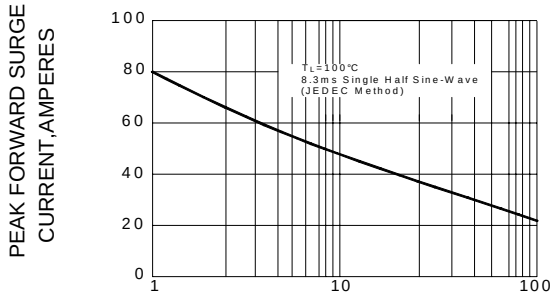


FIG.3 – TYPICAL FORWARD CHARACTERISTICS

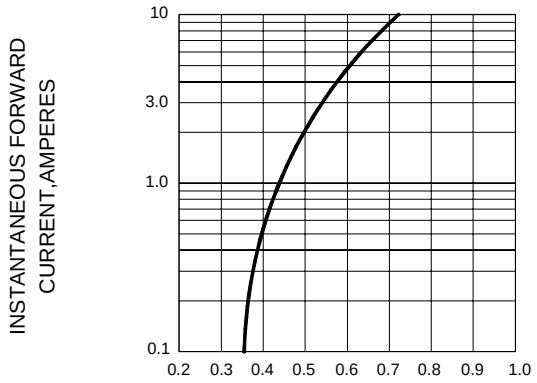


FIG.4 – TYPICAL REVERSE CHARACTERISTICS