

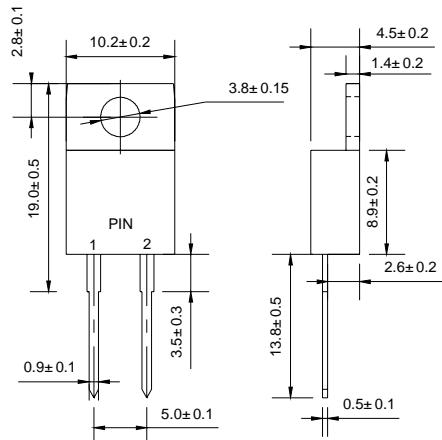
## TO-220AC

## FEATURES

- ◇ Low cost
- ◇ Diffuse junction
- ◇ Low forward voltage drop
- ◇ High current capability
- ◇ Easily cleaned with alcohol, Isopropanol and similar solvents
- ◇ The plastic material carries U/L recognition 94V-0

## MECHANICAL DATA

- ◇ Case: JEDEC TO-220AC, molded plastic
- ◇ Terminals: Solderable per MIL-STD-750, Method 2026
- ◇ Polarity: As marked
- ◇ Weight: 0.064 ounces, 1.96 gram
- ◇ Mounting position: Any



Dimensions in millimeters

## MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified.

Single phase, half wave, 60 Hz, resistive or inductive load. For capacitive load, derate by 20%.

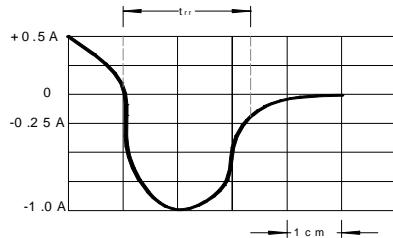
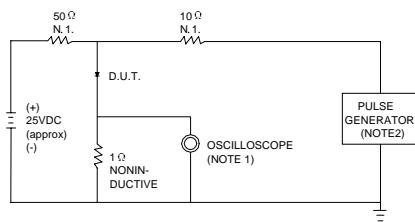
		MUR 810	MUR 820	MUR 830	MUR 840	MUR 850	MUR 860	UNITS				
Maximum recurrent peak reverse voltage	V <sub>RRM</sub>	100	200	300	400	500	600	V				
Maximum RMS voltage	V <sub>RMS</sub>	70	140	210	280	350	420	V				
Maximum DC blocking voltage	V <sub>DC</sub>	100	200	300	400	500	600	V				
Maximum average forward rectified current @ T <sub>C</sub> =100°C	I <sub>F(AV)</sub>	8						A				
Peak forward surge current 8.3ms single half-sine-wave superimposed on rated load @ T <sub>J</sub> =125°C	I <sub>FSM</sub>	150						A				
Maximum instantaneous forward voltage @ 8A	V <sub>F</sub>	0.98		1.3		1.7		V				
Maximum reverse current @ T <sub>A</sub> =25°C at rated DC blocking voltage @ T <sub>A</sub> =100°C	I <sub>R</sub>	10 400						µA				
Maximum reverse recovery time (Note1)	t <sub>rr</sub>	35						ns				
Typical junction capacitance (Note2)	C <sub>J</sub>	70		50				pF				
Typical thermal resistance (Note3)	R <sub>θJA</sub>	3.0						°C/W				
Operating junction temperature range	T <sub>J</sub>	-55 ----- +150						°C				
Storage temperature range	T <sub>STG</sub>	-55 ----- +150						°C				

NOTE: 1. Measured with I<sub>F</sub>=0.5A, I<sub>R</sub>=1A, t<sub>rr</sub>=0.25A.

2. Measured at 1.0MHz and applied reverse voltage of 4.0V DC.

3. Thermal resistance from junction to ambient.

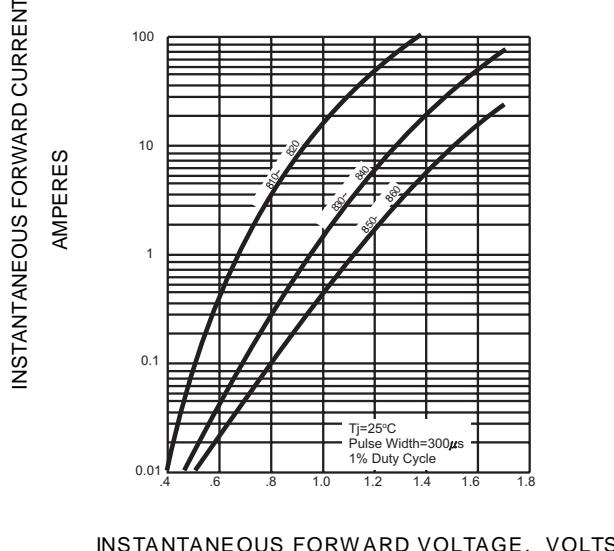
**FIG.1 -- TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTIC**



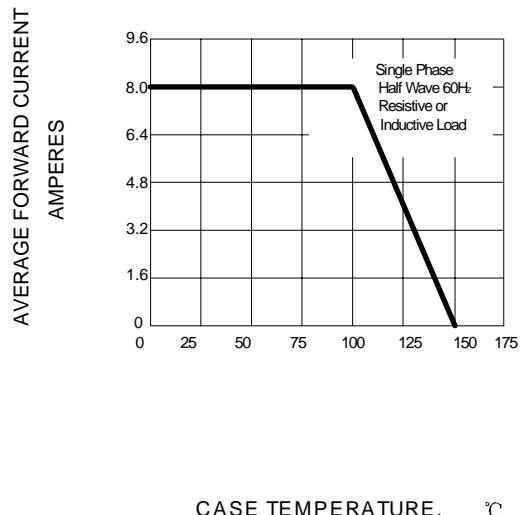
NOTES:  
1. RISE TIME = 7ns MAX.INPUT IMPEDANCE = 1M $\Omega$ .22pF.  
2. RISE TIME = 10ns MAX.SOURCE IMPEDANCE=50 $\Omega$ .

SET TIME BASE FOR 15 ns/cm

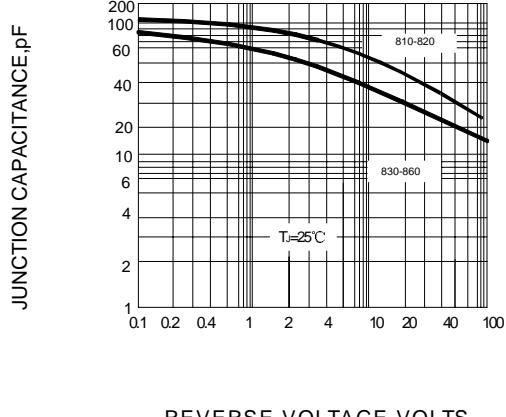
**FIG.2 -- TYPICAL FORWARD CHARACTERISTIC**



**FIG.3 -- FORWARD DERATING CURVE**



**FIG.4 -- TYPICAL JUNCTION CAPACITANCE**



**FIG.5 -- PEAK FORWARD SURGE CURRENT**

